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EPATENT APPLICATION	First Named Inventor or Application Identifier Naofal Al-Dhahir Title Finite-Length Multi-Input Multi-Output Channel			
TRANSMITTAL for new non-provisional applications under 37 CFR 1.53(b))	Shortening Pre-filters			
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title cross reference to related applications (e.g. provi	sional application)		wer of Attorney	
background	sional application)		stcard	
summary brief description of the drawings (if filed)		Sm	nall entity statement	
detailed description claims		_	rtified copy of priority documents	
abstract		_	ormation disclosure statement	
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a. Newly executed		⊠ che		
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(for continuations/divisionals with section below filled) Deletion of Inventor(s) Signed State inventor(s) named in the prior application and 1.33(b).	on. 37 CFR 163 (d)(2)			
Incorporation by reference (usable if Declaration) The entire disclosure of the prior application, from which a copy is supplied, is considered as being part of the disclosure of the supplied in part of the disclosure of the	y of the oath or declaration			
is hereby incorporated by reference herein.				
If a CONTINUING APPLICATION, check appropriate	box and supply the reg	uisite inform	mation:	
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I hereby certify that this Application is being deposited with to 37 CFR 1.10 on the date indicated above and is addressed to	ne United States Postal Se to the Assistant Commission	rvice "Expre oner for Pate	ess Mail Post Office to Addressee" service uents, Washington D.C. 20231.	ınder
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PTO/SB/17 Modified 12/98 - Henry Brendzel

FEE TRANSMITTAL

Patent Fees are subject to annual revisions on October 1
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Small entity payments must be supported by a small entity statement,
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Com	plete if Known		
Application Number	60/158,713	J.	
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First Named Inventor	Naofal Al-Dhahir	8. 18. 18.	
Examiner Name		7.00 D.00	
Group/Art Unit		92	
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METHOD OF PAYMENT (check one)						FEE CALCULATION (continued)		
The Commissioner is hereby authorized to charge indicated fees and other underpayments, and credit overpayments to:		3. ADDITIONAL FEES						
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2 CLAIMS	******				•		Petition to revive - unavoidable	
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SUBMITTED BY	Complete (if applicable)				
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Application Information

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Method and Apparatus for Designing

Finite-Length Multi-Input Multi-Output Channel Shortening pre-filters

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Finite-Length Multi-Input Multi-Output Channel Shortening Pre-filters

Related Application:

This application claims priority from Provisional application No. 60/158,713 filed on October 8, 1999. This application is also related to a Provisional application No. 60/158,714, also filed on October 8, 1999.

Background of the Invention:

The combination of maximum likelihood sequence estimation (MLSE) with receiver diversity is an effective technique for achieving high performance over noisy, frequency-selective, fading channels impaired by co-channel interference. With the addition of transmitter diversity, the resulting multi-input multi-output (MIMO) frequency-selective channel has a significantly higher capacity than its single-input multi-output (SIMO) or single-input single-output (SISO) counterparts. The use of maximum likelihood multi-user detection techniques on these frequency-selective MIMO channels significantly outperforms single-user detection techniques that treat signals from other users as colored noise. However, MLSE complexity increases exponentially with the number of inputs (or transmit antennas) and with the memory of the MIMO channel, making its implementation over sever inter-symbol interference (ISI) channels very costly.

The MIMO channel can be modeled as a collection of FIR filters (i.e., an FIR filter between each input point (e.g., transmitting antenna) and each receiving point (e.g. receiving antenna), and the "memory of the channel" corresponds to the number of taps in the FIR filters.

The Discrete Matrix Multitone (DMMT) was shown to be a practical transceiver structure that asymptotically achieves the MIMO channel capacity when combined with powerful codes. It uses the Discrete Fourier Transform (DFT) to partition the frequency responses of the underlying frequency-selective channels of the MIMO systems into a large number of parallel, independent, and (approximately) memoryless frequency subchannels. To eliminate inter-block and intra-block interference, a cyclic prefix whose length is equal to the MIMO channel

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memory is inserted in every block. On severe-ISI MIMO channels, the cyclic prefix overhead reduces the achievable DMMT throughput significantly, unless a large FFT size is used which, in turn, increases the computational complexity, processing delay, and memory requirements in the receiver.

In short, the computation complexity increases exponentially with the number of taps in the FIR filters that may be used to model the channel.

N. Al-Dhahir and J. M. Cioffi, in "Efficiently-Computed Reduced-Parameter Input-Aided MMSE Equalizers for ML Detection: A Unified Approach," IEEE Trans. Information Theory, pp. 903-915, May 1996, disclose use of a time-domain prefilter in the receiver to shorten the effective channel memory and hence reduce the cyclic prefix overhead and/or the number of MLSE states. The disclosed approach, however, is for SISO systems, and not for MIMO systems.

Summary

An advance in the art is realized with a MIMO pre-filter that is constructed from FIR filters with coefficients that are computed based on environment parameters that are designer-chosen. Given a transmission channel that is modeled as a set of FIR filters with memory ν , a matrix \mathbf{W} is computed for a pre-filter that results in an effective transmission channel \mathbf{B} with memory N_b , where $N_b < \nu$, where \mathbf{B} is optimized so that $\mathbf{B}_{opt} = \operatorname{argmin}_B \operatorname{trace}(\mathbf{R}_{ee})$ subject to selected constraints; \mathbf{R}_{ee} being the error autocorrelation function. The coefficients of \mathbf{W} , which are sensitive to a variety of designer constraints, are computed by a processor within pre-filter at the front end of a receiver and loaded into an array of FIR filters that form the pre-filter.

Brief Description of the Drawing

- FIG. 1 shows the major elements of a receiver in accord with the principles disclosed herein;
 - FIG. 2 presents the structure of pre-filter 30; and
- FIG. 3 is a flowchart describing the method carried out by processor 220 within pre-filter 30.

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Detailed Description

FIG. 1 depicts the general case of an arrangement with $n_{\rm q}$ transmitting antennas 11-1, 11-2, ... 11- $n_{\rm q}$, that output signals (e.g., space-time encoded signals) to a transmission channel, and $n_{\rm o}$ receiving antennas 21-1, 21-2, ... 21- $n_{\rm o}$. Each transmitting antenna p outputs a complex-valued signal x_p , the signals of the $n_{\rm q}$ antennas pass through a noisy transmission channel, and the $n_{\rm o}$ receiving antennas capture the signals that passed through the transmission channel. The received signals are oversampled by a factor of l in element 20 and applied to prefilter 30. Thus, the sampling clock at the output of element 20 is of period T_s =T/l, where T is the inter-symbol period at the transmitting antennas. The transmission channel's characterization is also referenced to T_s . In the illustrative embodiment disclosed herein, therefore, pre-filter 20 develops n_i output signals that are applied to a conventional multi-input receiver 40, and the received signal can be expressed by

$$y_k^{(j)} = \sum_{i=1}^{N} \sum_{m=0}^{v^{(i,j)}} h_m^{(i,j)} x_{k-m}^{(i)} + n_k^{(j)} , \qquad (1)$$

where $y_k^{(j)}$ is the signal at time k at the j^{th} receiving antenna, $h_m^{(i,j)}$ is the m^{th} coefficient (tap) in the channel impulse response between the i^{th} transmitting antenna and the j^{th} receiving antenna, and $\mathbf{n}^{(j)}$ is the noise vector at the j^{th} receiving antenna. The memory of this path (i.e., the largest value of m for which $h_m^{(i,j)}$ is not zero) is denoted by $v^{(i,j)}$. It not unreasonable to assume, however, that the memory of the transmission channel is the same for all i,j links ($n_i \times n_o$ such links), in which case $v^{(i,j)} = v$. Alternatively, the $v^{(i,j)}$ limit in equation (1) can be set to that v which corresponds to maximum length of all of the $n_i \times n_o$ channel input responses, i.e., $v = \max_{i,j} v^{(i,j)}$. All of these variables in equation (1) are actually $l \times 1$ column vectors, corresponding to the l time samples per symbol in the oversampled FIG. 1 arrangement. By grouping the received samples from all

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 n_o antennas at symbol time k into an $n_o l \times 1$ column vector \mathbf{y}_k , one can relate \mathbf{y}_k to the corresponding $n_i \times 1$ (column) vector of input samples as follows

$$\mathbf{y}_{k} = \sum_{m=0}^{\nu} \mathbf{H}_{m} \mathbf{x}_{k-m} + \mathbf{n}_{k} , \qquad (2)$$

where \mathbf{H}_{m} is the MIMO channel coefficients matrix of size $n_{o}I \times n_{i}$, \mathbf{x}_{k-m} is a size $n_{c} \times 1$ input (column) vector, and \mathbf{n}_{k} is a size $n_{o}I \times 1$ vector.

Over a block of N_f symbol periods, equation (2) can be expressed in matrix notation as follows:

$$\begin{bmatrix}
\mathbf{y}_{k+N_{r-1}} \\
\mathbf{y}_{k+N_{r-2}} \\
\vdots \\
\mathbf{y}_{k}
\end{bmatrix} = \begin{bmatrix}
\mathbf{H}_{0} & \mathbf{H}_{1} & \cdots & \mathbf{H}_{v} & 0 & \cdots & 0 \\
0 & \mathbf{H}_{0} & \mathbf{H}_{1} & \cdots & \mathbf{H}_{v} & 0 & \cdots \\
\vdots & \vdots & \vdots & \vdots & \vdots & \ddots & \vdots \\
0 & \cdots & 0 & \mathbf{H}_{0} & \mathbf{H}_{1} & \cdots & \mathbf{H}_{v}
\end{bmatrix} \begin{bmatrix}
\mathbf{x}_{k+N_{r-1}} \\
\mathbf{x}_{k+N_{r-2}} \\
\vdots \\
\mathbf{x}_{k-v}
\end{bmatrix} + \begin{bmatrix}
\mathbf{n}_{k+N_{r-1}} \\
\mathbf{n}_{k+N_{r-2}} \\
\vdots \\
\mathbf{n}_{k}
\end{bmatrix}$$
(3)

or, more compactly,

$$\mathbf{y}_{k+N_{r}-1;k} = \mathbf{H}\mathbf{x}_{k+N_{r}-1;k-\nu} + \mathbf{n}_{k+N_{r}-1;k}. \tag{4}$$

The subscripts in equation (4) indicate a range. For example $k + N_f - 1$: k indicates the range $k + N_f - 1$ and k, inclusive.

It is useful to define the following correlation matrices:

$$R_{xy} = E[x_{k+N_{*}-1}, y_{k+N_{*}-1}, y_{k+N_{*}-1}, y_{k+N_{*}-1}] = R_{xx}H^{*}$$
(5)

$$\mathbf{R}_{yy} = E[y_{k+N_{r}-1:k}y_{k+N_{r}-1:k}^{*}] = \mathbf{H}\mathbf{R}_{xx}\mathbf{H}^{*} + \mathbf{R}_{nn},$$
 (6)

$$\mathbf{R}_{xx} = E[x_{k+N_{r}-1:k-\nu}x_{k+N_{r}-1:k-\nu}^{*}] \text{ and}$$
 (7)

$$R_{nn} = E[n_{k+N_{r-1},k}n_{k+N_{r-1},k}^{*}]. \tag{8}$$

It is assumed that these correlation matrices do not change significantly in time or, at least, do not change significantly over a time interval that corresponds to a TDMA burst (assumed to be much shorter than the channel coherence time), which is much longer than the length of the pre-filter, in symbol periods denoted by $N_{\rm f}$. Accordingly, a re-computation of the above matrices, and the other parameters disclosed herein, leading to the computation of pre-filter coefficients, need not take place more often than once every TDMA burst.

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Once H, R_{xx} and R_{nn} and known, R_{xy} and R_{yy} are computed by $R_{xx}H^*$ and $HR_{xx}H^* + R_{nn}$, respectively.

Given the MIMO channel matrix \mathbf{H} with $\nu+1$ members $(\mathbf{H}_0,\mathbf{H}_1,\cdots\mathbf{H}_{\nu})$, the objective is to create a MIMO pre-filter \mathbf{W} (element 30 in FIG. 1) with N_f matrix taps, i.e., matrix $\mathbf{W} \equiv \begin{bmatrix} \mathbf{W}_0 & \mathbf{W}_1 & \cdots & \mathbf{W}_{N_f-1} \end{bmatrix}^T$, that equalizes \mathbf{H} so as to create an overall transmission channel for receiver 40 that corresponds to a matrix \mathbf{B} with memory N_b , where $N_b << \nu$.

The matrix **B** can be expressed as $\mathbf{B} = \begin{bmatrix} \mathbf{B}_0 & \mathbf{B}_1 & \cdots & \mathbf{B}_{N_b} \end{bmatrix}^T$ where each \mathbf{B}_i is of size $n_i \times n_j$.

The MIMO channel-shortening pre-filter **W** (element 30) is conditioned, or adjusted, to minimize the equalization Mean Squared Error (MSE), defined by $MSE \equiv trace(R_{ee}) \text{ , where } R_{ee} \text{ is the autocorrelation matrix of the error vector } \textbf{E}_k$ that is given by

$$\mathbf{E}_{k} = \tilde{\mathbf{B}}^{*} \mathbf{x}_{k+N_{r}-1:k-\nu} - \mathbf{W}^{*} \mathbf{y}_{k+N_{r}-1:k} , \qquad (9)$$

where the augmented MIMO matrix, $\tilde{\mathbf{B}}^{\star}$, is

$$\tilde{\mathbf{B}}^{\star} \equiv \begin{bmatrix} \mathbf{0}_{n_{i} \times n_{i} \Delta} & \mathbf{B}_{0}^{\star} & \mathbf{B}_{1}^{\star} & \cdots & \mathbf{B}_{N_{b}}^{\star} & \mathbf{0}_{n_{i} \times n_{i} S} \end{bmatrix} \equiv \begin{bmatrix} \mathbf{0}_{n_{i} \times n_{i} \Delta} & \mathbf{B}^{\star} & \mathbf{0}_{n_{i} \times n_{i} S} \end{bmatrix}, \tag{10}$$

 Δ is the decision delay that lies in the range $0 \le (N_f + v - N_b - 1)$, and

 $s = N_f + v - N_b - \Delta - 1$. The $n_i \times n_i$ error autocorrelation function \mathbf{R}_{ee} can be expressed by the following:

$$\mathbf{R}_{ee} \equiv E[\mathbf{E}_{k}\mathbf{E}_{k}^{*}]$$

$$= \tilde{\mathbf{B}}^{*}(\mathbf{R}_{xx} - \mathbf{R}_{xy}\mathbf{R}_{yy}^{-1}\mathbf{R}_{yx})\tilde{\mathbf{B}}$$

$$= \tilde{\mathbf{B}}^{*}\mathbf{R}^{\perp}\tilde{\mathbf{B}}$$

$$= \tilde{\mathbf{B}}^{*}\bar{\mathbf{R}}\mathbf{B},$$
(11)

where $\overline{\mathbf{R}}$ is a sub-matrix of \mathbf{R}^{\perp} determined by Δ .

Using the orthogonality principle, which states that $E[\mathbf{E}_k \mathbf{y}_{k+N_r-1:k}^*] = 0$ it can be shown that the optimum channel-shortening pre-filter and target impulse response filters (**W** and **B**, respectively) are related by

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$$\mathbf{W}_{opt}^{*} = \tilde{\mathbf{B}}_{opt}^{*} \mathbf{R}_{xy} \mathbf{R}_{yy}^{-1}$$

$$= \tilde{\mathbf{B}}_{opt}^{*} \mathbf{R}_{xx} \mathbf{H}^{*} (\mathbf{H} \mathbf{R}_{xx} \mathbf{H}^{*} + \mathbf{R}_{nn})^{-1}$$

$$= \tilde{\mathbf{B}}_{opt}^{*} (\mathbf{R}_{xx}^{-1} + \mathbf{H}^{*} \mathbf{R}_{nn}^{-1} H)^{-1} \mathbf{H}^{*} \mathbf{R}_{nn}^{-1}.$$
(12)

The last line shows explicitly that the MIMO channel-shortening pre-filter consists of a noise whitener \mathbf{R}_{nn}^{-1} , a MIMO matched filter \mathbf{H}^* , and a bank of FIR channel-shortening pre-filter elements.

It remains to optimize $\tilde{\mathbf{B}}$ such that the MSE is minimized, which may be obtained by computing the parameters of \mathbf{B} that, responsive to specified conditions, minimizes the trace (or determinant) of \mathbf{R}_{ee} . The following discloses two approaches to such optimization.

Under one optimization approach the coefficients of **B** are constrained so that some coefficient of **B** is equal to the identity matrix, **I**. A solution subject to this Identity Tap Constraint (ITC) can be expressed by

$$\mathbf{B}_{opt}^{TC} = \operatorname{argmin}_{B} \operatorname{trace}(\mathbf{R}_{ee}) \text{ subject to } \mathbf{B}^{*} \mathbf{\Phi} = \mathbf{I}_{n}, \tag{13}$$

where $\Phi^* \equiv \begin{bmatrix} \mathbf{0}_{n_i \times n_i m} & \mathbf{I}_{n_i} & \mathbf{0}_{n_i \times n_i (N_b - m)} \end{bmatrix}$ and $0 \le m \le N_b$. It can be shown that the optimum MIMO target impulse response and the corresponding error autocorrelation matrix are given by

$$\mathbf{B}_{opt}^{ITC} = \overline{\mathbf{R}}^{-1} \mathbf{\Phi} (\mathbf{\Phi}^* \overline{\mathbf{R}}^{-1} \mathbf{\Phi})^{-1} \text{ and}$$
 (14)

$$\mathbf{R}_{\text{ee,min}}^{\text{ITC}} = (\mathbf{\Phi}^* \mathbf{\bar{R}}^{-1} \mathbf{\Phi})^{-1}. \tag{15}$$

As indicated above, $\bar{\mathbf{R}}$ is affected by the delay parameter Δ . Unless dictated by the designer, the delay parameter Δ , which can range between 0 and $(N_f + \nu - N_b - 1)$, is chosen to minimize the trace of $\mathbf{R}_{\text{ee,min}}^{ITC}$. Similarly, the index parameter m, which ranges between 0 and N_b , and which that affects Φ , is chosen to minimize the trace of $\mathbf{R}_{\text{ee,min}}^{ITC}$.

Under a second optimization approach the imposed constraint is $\mathbf{B}^*\mathbf{B} = \mathbf{I}_{n_i}$. A solution subject to this Ortho-Normality Constraint (ONC) can be expressed by

$$\mathbf{B}_{opt}^{ONC} = \operatorname{argmin}_{B} \operatorname{trace}(\mathbf{R}_{ee}) \text{ subject to } \mathbf{B}^{\star} \mathbf{B} = \mathbf{I}_{n_{e}}, \tag{16}$$

Defining the eigen-decomposition

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$$\bar{\mathbf{R}} = \mathbf{U}\boldsymbol{\Sigma}\mathbf{U}^* = \mathbf{U}\operatorname{diag}(\sigma_0, \sigma_1 \cdots \sigma_{n,(N_{k+1})-1})\mathbf{U}^*, \tag{17}$$

where $\sigma_0 \ge \sigma_1 \cdots \ge \sigma_{n,(N_b+1)-1}$, then the optimum MIMO target response and the resulting error autocorrelation matrix are given by

$$\mathbf{B}_{opt}^{ONC} = \mathbf{U} \Big[\mathbf{e}_{n,N_b} \quad \cdots \quad \mathbf{e}_{n_c(N_b+1)-1} \Big], \tag{18}$$

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$$\mathbf{R}_{\text{ee,min}}^{ONC} = diag(\sigma_{n,N_b}, \dots, \sigma_{n,(N_b+1)-1}). \tag{19}$$

Illustratively, if n_i =3 and N_b =3, $\mathbf{B}_{opt}^{ONC} = \mathbf{U}\big[e_9, e_{10}, e_{11}\big]$, meaning that \mathbf{B}_{opt}^{ONC} is a three column matrix comprising the 9th through the 11th columns of matrix \mathbf{U} . Stated in words, the optimum MIMO target impulse response matrix is given by the n_i eigenvectors of \mathbf{R} that correspond to its n_i smallest eigenvalues. The delay parameter Δ $(0 \le \Delta \le N_f + \nu - N_b - 1)$ that affects \mathbf{R} is optimized to minimize the trace (or determinant) of $\mathbf{R}_{\text{ee,min}}^{ONC}$.

With the above analysis in mind, a design of a prefilter 30 can proceed for any given set of system parameters, which includes:

- MIMO channel memory between the input points and the output point of the actual transmission channel, ν ,
- The number of pre-filter taps chosen, N_f,
- The shortened MIMO memory, N_b,
- The number of inputs to the transmission channel, n_i
- The number of output derived from the transmission channel, n_0 ,
- The autocorrelation matrix of the inputs, R_{xx},
- The autocorrelation matrix of the noise, R_{nn},
- The oversampling used, I, and
- The decision delay, Δ.

The structure of pre-filter 30 is shown in FIG. 2. In the illustrated embodiment, it comprises two main components: processor 220 and filter section 210.

Filter section 210 in the FIG. 2 illustrative example comprises a collection of FIR filters that connect the n_o input array of signals from sampling circuit 20 to an

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 n_i output array of points. That is, there are $j \times i$ FIR filters $P_{j,i}$, that couple input point j to output point i.

Processor 220 is responsive to the n_o signals received by antennas 21 and sampled by circuit 20, and it computes the coefficients of W, as disclosed above. W₀ is a matrix that defines the coefficients in the 0th tap of the $j \times i$ FIR filters, W₁ is a matrix that defines the coefficients in the 1st tap of the $j \times i$ FIR filters, etc.

The method of developing the parameters of pre-filter 30, carried out in processor 220, is shown in FIG. 3. Block 100 develops an estimate of the MIMO channel between the input points and the output point of the actual transmission channel. This is accomplished in a conventional manner through the use of training sequences. The estimate of the MIMO channel can be chosen to be limited to a given memory length, ν , or can be allowed to include as much memory as necessary to reach a selected estimate error level. That, in turn, depends on the environment and is basically equal to the delay spread divided by T_s .

Following step 100, step 110 determines the matrices, \mathbf{R}_{nn} , \mathbf{R}_{xx} , \mathbf{R}_{xy} , and \mathbf{R}_{yy} . The matrix \mathbf{R}_{nn} is computed by first computing $\mathbf{n} = \mathbf{y} - \mathbf{H}\mathbf{x}$ and then computing the expected value $E[\mathbf{n}^*\mathbf{n}]$ -- see equation (8) above. The matrix \mathbf{R}_{xx} is computed from the known training sequences -- see equation (7) above -- (or is precomputed and installed in processor 220). In may be noted that for uncorrelated inputs, \mathbf{R}_{xx} =I. The matrices \mathbf{R}_{xy} and \mathbf{R}_{yy} are computed from the known training sequences and the received signal or directly from \mathbf{H} and \mathbf{R}_{nn} -- see equations (5) and (6) above.

Following step 110, step 120 computes $\mathbf{R}^{\perp} = \mathbf{R}_{xx} - \mathbf{R}_{xy} \mathbf{R}_{yy}^{-1} \mathbf{R}_{yx}$, and the submatrix $\mathbf{\bar{R}}$. From equation (10) is can be seen that $\mathbf{\bar{R}}$ is obtained by dropping the first $n_i \Delta$ rows and the last $n_i s$ rows of \mathbf{R}^{\perp} .

In accordance with the ITC approach, selecting some value of $0 \le m \le N_b$ allows completion of the design process. Accordingly, following step 120, step 130 chooses a value for m, develops $\mathbf{\Phi}^* \equiv \begin{bmatrix} \mathbf{0}_{n_i \times n_i m} & \mathbf{I}_{n_i} & \mathbf{0}_{n_i \times n_i (N_b - m)} \end{bmatrix}$ and carries out

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the computation of equation (13). Step 140 finally develops the coefficients of matrix W in accordance with equation (12), and installs the developed coefficients within filter 210.

In accordance with the ONC approach, step 130 computes the matrix \mathbf{U} in a conventional manner, identifies the unit vectors e_i , and thus obtains the matrix \mathbf{B} . Step As with the ITC approach, step 140 develops the coefficients of matrix \mathbf{W} in accordance with equation (12), and installs the developed coefficients within filter 210.

It should be understood that a number of aspects of the above disclosure, for example, those related to the ITC constraint and to the ONC constraint, are merely illustrative, and that persons skilled in the art may make various modifications that, nevertheless, are within the spirit and scope of this invention. For example, the pre-filter described above generates a multi-output signal, with the number of outputs being n_i , that being also the number of transmitting antennas 11. This, however, is not a limitation of the principles disclosed herein. The number of pre-filter outputs can, for example, be larger than n_i , for example as high as $n_i(N_b+1)$. The performance of the receiver will be better with more filter outputs, but more outputs require more FIR filters, more FIR filter coefficients, and correspondingly, a greater processing power requirement placed on processor 220.

Claims:

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1. A receiver responsive to an n_o plurality of antennas comprising: a pre-filter having an $n_o \times n_i$ plurality of FIR filters, each responsive to a signal that is derived from one of said n_o antennas and applied to an input point, and each developing an output signal that contributes to one of n_i pre-filter outputs; and

decision logic responsive to said n_i outputs.

- **2.** The receiver of claim **1** further comprising a sampling circuit interposed between said n_o plurality of antennas and said pre-filter that samples received signal at rate $T_s = \frac{T}{I}$, where I is an integer and T is symbol rate of a transmitter whose signals said receiver receives.
 - 3. The receiver of claim 2 where I>1.
- **4.** The receiver of claim **1** where coefficients of said FIR filters are computed in a processor in response to a block of N_f symbols.
 - 5. The receiver of claim 4 where said processor is part of said pre-filter.
- **6.** The receiver of claim **4** where said coefficients of said FIR filters are computed once every time interval during which transfer characteristics of said transmission channel, **H**, are substantially constant.
- 7. The receiver of claim 6 where said processor installs computed coefficients of said FIR filters in said FIR filters following each computation.

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- **8.** The receiver of claim **1** where said FIR filters form an array of filters, and said array has one FIR filter connected between each of said n_o input points and said n_i outputs.
- **9.** The receiver of claim **8** where said n_0 plurality of antennas receive signals, via said transmission channel, from a transmitter having a multiple number of transmitting antennas.
- **10.** The receiver of claim **9** where said transmitter has n_i transmitting antennas.
 - **11.** The receiver of claim **10** wherein said decision logic is adapted to receive from said transmitted signals that were encoded in a space-time encoding schema.
 - 12. The receiver of claim 2 where said plurality of FIR filters is expressed by matrix **W**, and **W** is computed by $\mathbf{W}_{opt}^* = \tilde{\mathbf{B}}_{opt}^* \mathbf{R}_{xy} \mathbf{R}_{yy}^{-1}$,

 $\mathbf{W}_{opt}^{\star} = \mathbf{\tilde{B}}_{opt}^{\star} \mathbf{R}_{xx} \mathbf{H}^{\star} (\mathbf{H} \mathbf{R}_{xx} \mathbf{H}^{\star} + \mathbf{R}_{nn})^{-1}$, or $\mathbf{W}_{opt}^{\star} = \mathbf{\tilde{B}}_{opt}^{\star} (\mathbf{R}_{xx}^{-1} + \mathbf{H}^{\star} \mathbf{R}_{nn}^{-1} \mathbf{H})^{-1} \mathbf{H}^{\star} \mathbf{R}_{nn}^{-1}$, where \mathbf{R}_{xx} is an autocorrelation matrix of a block of signals transmitted by a plurality of transmitting antennas to said n_o antennas via a channel having a transfer characteristic \mathbf{H} , \mathbf{R}_{nn} is an autocorrelation matrix of noise received by said plurality of n_o antennas during said block of signals transmitted by said transmitting antennas, $\mathbf{R}_{xy} = \mathbf{R}_{xx} \mathbf{H}^{\star}$, $\mathbf{R}_{yy} = \mathbf{H} \mathbf{R}_{xx} \mathbf{H}^{\star} + \mathbf{R}_{nn}$, and

 $\tilde{\mathbf{B}}_{opt}^{\star}$ is a sub-matrix of matrix \mathbf{B}_{opt}^{\star} , where $\mathbf{B}_{opt} = \operatorname{argmin}_{B} \operatorname{trace}(\mathbf{R}_{ee})$ subject to a selected constraint, \mathbf{R}_{ee} being the error autocorrelation function.

13. The receiver of claim 12 wherein said plurality of FIR filters are subjected to designer constraints relative to any one or a number of members of the following set: transmission channel memory, size of said block, effective memory of the combination consisting of said transmission channel and said pre-

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filter; n_i , n_o , autocorrelation matrix \mathbf{R}_{xx} , autocorrelation matrix \mathbf{R}_{nn} , value of factor I in said sampling circuit, and decision delay.

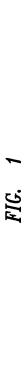
- 14. The receiver where said matrix W is expressible by
- 5 $\mathbf{W} = \begin{bmatrix} \mathbf{W}_0 & \mathbf{W}_1 & \cdots & \mathbf{W}_{N_r-1} \end{bmatrix}^t$, where matrix \mathbf{W}_q is a matrix that specifies \mathbf{q}^{th} tap coefficients of said FIR filters.
 - **15.** The receiver of claim **12** where said constraint restricts **B** so that $\mathbf{B}^* \mathbf{\Phi} = \mathbf{I}_{n_i}$, where $\mathbf{\Phi}^* \equiv \begin{bmatrix} \mathbf{0}_{n_i \times n_i m} & \mathbf{I}_{n_i} & \mathbf{0}_{n_i \times n_i (N_b m)} \end{bmatrix}$ and m is a selected constant.
 - **16.** The receiver of claim **15** where $\mathbf{B} = \overline{\mathbf{R}}^{-1} \Phi (\Phi^* \overline{\mathbf{R}}^{-1} \Phi)^{-1}$, $\overline{\mathbf{R}}$ is a sub-matrix of a matrix $\mathbf{R}^{\perp} = \mathbf{R}_{xx} \mathbf{R}_{xy} \mathbf{R}_{yy}^{-1} \mathbf{R}_{yx}$.
 - 17. The receiver of claim 12 where said constraint restrict **B** so that $\mathbf{B}^*\mathbf{B} = \mathbf{I}_n$.
 - **18.** The receiver of claim **17** where $\mathbf{B} = \mathbf{U} \Big[e_{n,N_b} \cdots e_{n,(N_b+1)-1} \Big]$, each element e_p is a vector having a 0 element in all rows other than row p, at which row the element is 1, and U is a matrix that satisfies the equation $\mathbf{R} \equiv \mathbf{U} \mathbf{\Sigma} \mathbf{U}^*$, $\mathbf{\Sigma}$ being a diagonal matrix.

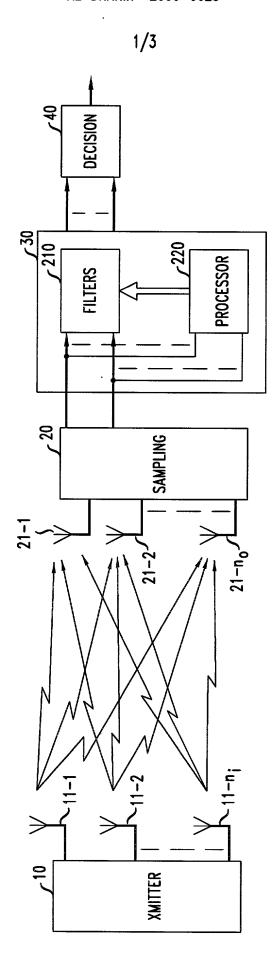
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<u>Abstract</u>

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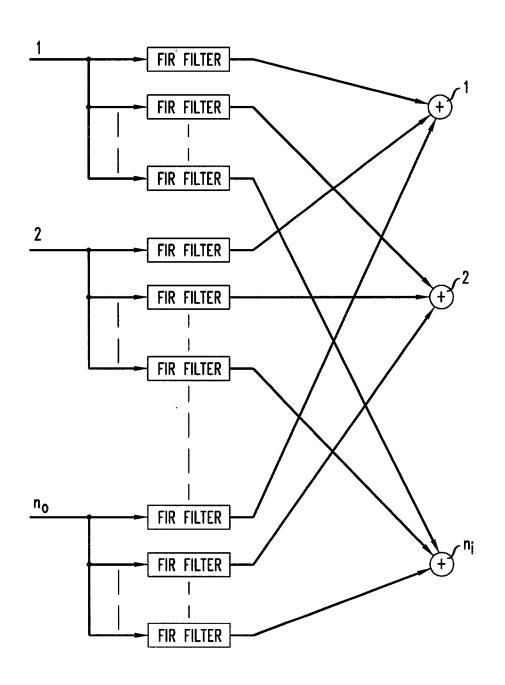
A multi-input, multi-output pre-filter improves operation of a multi-input receiver by shortening the effective memory of the channel with a set of FIR filters. The coefficients of these FIR filters can be fashioned to provide a variety of controls by the designer, for example, the value of the effective memory.

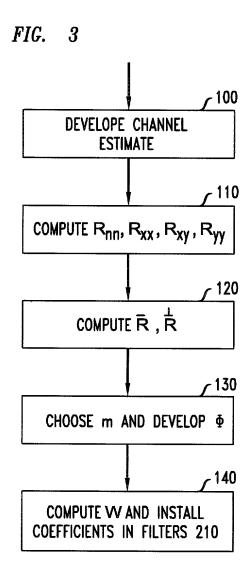




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FIG. 2





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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Declaration and Power of Attorney

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am an original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled **Finite-Length Multi-Input Multi-Output Channel Shortening Pre-filters** the specification of which was filed on 10/8/99, as application Serial No. 60/158,713.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by an amendment, if any, specifically referred to in this oath or declaration.

I acknowledge the duty to disclose all information known to me, which is material to patentability as defined in Title 37, Code of Federal Regulations, 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, 119 of any foreign application(s) for patent or inventors' certificate listed below and have also identified below any foreign application for patent or inventors' certificate having a filing date before that of the application on which priority is claimed:

I hereby claim the benefit under Title 35, United States Code, 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, 112, we acknowledge the duty to disclose all information known to us to be material to patentability as defined in Title 37, Code of Federal Regulations, 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

US Provisional application 60/158,713, filed October 8, 1999.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby appoint the following attorney(s) with full power of substitution and revocation, to prosecute said application, to make alterations and amendments therein, to receive the patent, and to transact all business in the Patent and Trademark Office connected therewith:

Samuel H. Dworetsky	(Reg. No. 27873)	Thomas A. Restaino	(Reg. No. 33444)
Michele L. Conover	(Reg. No. 34962)	Cedric G. DeLaCruz	(Reg. No. 36498)
Rohini K. Garg	(Reg. No. 45272)	Thomas M. Isaacson	(Reg. No. 44166)
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I also appoint the following as associate attorney(s), with full power to prosecute said application, to make alternations and amendments therein, and to transact all business in the Patent and Trademark Office connected therewith:

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